FIBULA most useful macros

(To see the whole list do HELP | View Instructions | Macros)

BUF	Reserve data buffer
BUFM	Reserve Modulo Buffer
BUFR	Reserve FFT Buffer
CLOCK	Change DSP clock frequency
CMAT	Create constant Matrix
CN	Connect 24 bit data
CNA	Connect using Accumulator
CNB	Connect Boolean
CNC	Connect Complex
CND	Connect Double (48 bit) data
CNM	Connect Matrix
CNX	Connect Indexed
COEF	Create 24 bit Coefficient in Y:
COEFC	Create Complex (2 x 24 bit) coefficient in Y:
COEFD	Create Double precision (48 bit) coefficient in Y:
CONST	Define a Constant
CONSTC	Define a Complex Constant
FFT	Calculate Fast Fourier Transform of a complex data buffer
FLAGCLR	Set a flag to 0
FLAGSET	Set a flag to 1
FLAGTOG	Change value of a flag
GOTO	Branch to a label
JCF	Jump to label if Flag and clear flag
JCNF	Jump to label if Not Flag and Clear flag
JF	Jump to label if Flag
JNF	Jump to label if Not Flag
JSF	Jump to label if flag set and set flag
JTF	Jump to label if Flag and Toggle flag
JTNF	Jump to label if Not flag and Toggle flag
LDA	Load accumulator A from a variable
LDB	Load accumulator B from a variable
LDX0	Load register X0 from 24 bit variable
LDX1	Load register X1 from 24 bit variable
LDX	Load register X from 48 bit variable
LDY0	Load register Y0 from 24 bit variable
LDY1	Load register Y1 from 24 bit variable
LDY	Load register Y from 48 bit variable
MAT	Reserve a variable matrix
PTR	Define a S/W pointer
PTRU	Update pointer
STA	Store accumulator A into a variable
STB	Store accumulator B into a variable
STX0	Store register X0 into 24 bit variable
STX1	Store register X1 into 24 bit variable
STX	Store register X into 48 bit variable
STY0	Store register Y0 into 24 bit variable
STY1	Store register Y1 into 24 bit variable
STY	Store register Y into 48 bit variable
TIC	Start cycle counting
TOC	Stop and display cycle counting
VAR	Reserve 24 bit variable in X:
VARC	Reserve Complex 2 x 24 bit variable in X:
VARD	Reserve 48 bit variable in L:
WAICF	Wait for a flag and then clear this flag